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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****
 JAPAN 2002-301774 10/16/2002

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 1	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged <i>N. J. Lavelle</i> Examiner's Signature	<i>ML</i> Initials		

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TITLE
 Area array semiconductor device and electronic circuit board utilizing the same

FILING FEE RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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